



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Dong-Hun Lee, et al.

Examiner: Lindsay Jr., Walter Lee

Serial No.: 10/785,268

Art Unit: 2812

Filed: February 24, 2004

Docket No.: 8054-46 (AW8136US/MJ)

For: **METHOD OF MANUFACTURING A MOS TRANSISTOR OF A SEMICONDUCTOR DEVICE**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT AND RESPONSE TO OFFICE ACTION**

Sir:

In response to the Office Action mailed September 30, 2005, please amend the above-identified application as follows:

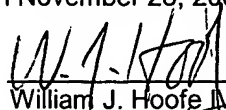
**Amendments to the Claims** are reflected in the listing of claims that begins on page 2 of this paper.

**Remarks** begin on page 9 of this paper.

**CERTIFICATE OF MAILING 37 C.F.R. § 1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on November 28, 2005.

Dated: November 28, 2005

  
William J. Hoofe IV